



UNIVERSITÀ DEGLI STUDI DELL'AQUILA

Prof. Giulio Antonini Curriculum scientifico

(Aggiornato il 2020/09/19)

Giulio Antonini (M'94) received the Laurea degree (summa cum laude) in Electrical Engineering in 1994 from the University of L'Aquila and the Ph.D. degree in Electrical Engineering in 1998 from the University of Rome "La Sapienza". Since 1998 he has been with the UAq EMC Laboratory, Department of Electrical Engineering of the University of L'Aquila where he is actually Associate Professor. His research interests focus on EMC analysis, numerical modeling and in the field of Signal Integrity for high-speed digital systems. In 1998 he received the Best Transactions Paper Award for the best paper published in the IEEE Transactions on Electromagnetic Compatibility in 1997. In 2004, 2005 and 2006 he was awarded with the IBM Shared University Research (SUR) Award. In 2004 he received the CST University Publication Award for the best paper based on Finite Integration Technique. In 2006 he received a Technical Achievement Award from the IEEE EMC Society "For innovative contributions to computational electromagnetic on the Partial Element Equivalent Circuit (PEEC) technique for EMC applications". He is the recipient of the 2007 IET-SMT Best Paper Award. Since 1998 he collaborates with the IBM T.J. Watson Research Center (New York) in the development of algorithms for PEEC modeling. Prof. Antonini has been Chair of the IEEE EMC Italy Chapter, the Chair of the TC-10 Committee and is actually a member of the TC-9 committee of the IEEE EMC Society. He has been Distinguished Lecturer of the IEEE EMC Society for the period 2010-2011. He serves as member of the Editorial Board of IET Science, Measurements and Technology and as reviewer in a number of IEEE-IET journals.

Journal papers

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[Elenco pubblicazioni \(dall'Archivio Istituzionale IRIS\)](#)

Honors

1. IEEE Electromagnetic Compatibility Society 1998 Prize Paper Award per l'articolo:
G. Antonini, S. Cristina, A. Orlandi, "A Spice Model for Near Field Transient Analysis of Ferromagnetic Grids", IEEE Transactions on Electromagnetic Compatibility, vol. 39, n. 2, pp. 114-122, May 1997, pubblicato nel 1997 sulla rivista internazionale IEEE Transactions on Electromagnetic Compatibility.
2. 2004 IBM Shared University Research (SUR) Award per lo sviluppo di modelli equivalenti di dielettrici dispersivi e dissipativi (PEEC modeling of lossy and dispersive dielectrics).
3. 2005 IBM Shared University Research (SUR) Award per lo sviluppo di modelli di ASICs di grandi dimensioni (Equivalent circuit modeling of large ASICs for power integrity analysis).
4. 2006 IBM Shared University Research (SUR) Award per lo sviluppo della tecnica Feature Selective Validation (FSV) (A tool for computational electromagnetic validation).
5. 2004 CST University Publication Award per la migliore pubblicazione scientifica basata sul metodo Finite Integration Technique (FIT):
G. Antonini, A. Ciccomancini Scogna, A. Orlandi, "S-Parameters Characterization of Through, Blind and Buried Via Holes", in IEEE Transactions on Mobile Computing, vol. 2, n. 2, pp. 174-184, Aprile-Giugno 2003.
6. Technical Achievement Award della IEEE Electromagnetic Compatibility Society "For innovative contributions to computational electromagnetic on the Partial Element Equivalent Circuit (PEEC) technique"

for EMC applications?.

7. 2007 IET Science, Measurement and Technology Best Paper Award.

8. 2009 Certificate of Acknowledgement of the IEEE EMC Society.

9. 2010-2011 IEEE EMC Society Distinguished Lecturer.